Synthesizing Efficient Pulses for Practical Qudit Circuits

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ABSTRACT

Many underlying qubit technologies have natural access to higher energy states which can expand available computational space.

Rather than considering binary two-level abstractions or qubits, using 3 or 4 logical states or qudits temporarily enables efficient circuit decompositions - reduced depth and physical device requirements.

Unfortunately, gate and coherence errors and gate durations are expected to scale unfavorably making use of these additional states challenging and demands careful engineering of pulse level control to minimize empirical costs.

We explore the engineering and design of pulses for mixed-radix quantum systems and introduce a compiler tool-chain to make context-dependent communication and compression choices.

BACKGROUND

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Superconducting qubits have a large spectrum of potential energy states which can be used as logical states for quantum computation.

Prior work [5] has identified circuit-level advantages of "compression" resulting in low depth circuits without need for ancilla like Toffoli and arithmetics.

Theoretically, gate durations scale quadratically [6] by using higher states which mitigates effectiveness without specialized pulse engineering and compilation.



Selected References

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QUDIT PULSE ENGINEERING

Quantum Optimal Control (QOC) is a technique for designing control pulses to purposefully manipulate a quantum system.

Multi-gudit drift Hamiltonian:



Time-dependent control:



The goal of QOC is to find optimal control functions f^k to realize desired gates. To this end we use the software packages Jugbox [1, 2] and Quandary [3, 4], which solve the QOC problem in the rotating wave approximation.



They find corresponding rotating frame controls p^k and q^k , which are parametrized with envelopes shaped by B-spline basis functions enclosing carrier waves, with coefficients $\vec{\alpha}^k$. Carrier wave frequencies are chosen to be resonant frequencies of the system.





 $0\rangle \rightarrow |2\rangle$

 $|21\rangle \rightarrow |23\rangle$

 $22\rangle \rightarrow |20\rangle$









Available Quguart Compilation Gates

	Devices Used	Qudit 1 State	Qudit 2 State	CX Time (ns)	SWAP Time (ns)
	$\bigcirc \bullet$	Ququart	-	80	64
	00	Qubit	Qubit	264	564
	00	Qubit	Ququart	-	-
	$\bigcirc \bullet$	Qubit	Ququart	-	
-		Ququart	Qubit	-	
	00	Ququart	Ququart	768	-
-	$\bigcirc \bullet$	Ququart	Ququart	64+768+64	-
-		Ququart	Ququart	64+768+64	-
		Ququart	Ququart	64+768+64	-
	00	Ququart	Ququart	n/a	1280

Compilation Constraint: Qubits can only interact when adjacent to one another, with limited connectivity must move qubits on device with minimal number of gates.

Compiling with Qudits: With increased connectivity, we can encode gubits onto the architecture and route with more flexibility, reducing the run time of the circuit.

Expanded Library: Current times are shown, and are the bare minimum for compiling on ququarts. Optimized pulses will provide more beneficial compilations.



Qubits

Example Circuit

Compiled With Compiled on Ququarts with Encoded Qubits